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PATENT NUMBER and  
ISSUE DATE  
**6515910**

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10076826	02/15/2002	365	155.22	2818	HOANG, HUAN

\*\*APPLICANTS: Lee Peter; Tsao Hsing-Ya; Tran Tam; Hsu Fu-Chang;

\*\*CONTINUING DATA VERIFIED: *W*  
THIS APPLN CLAIMS BENEFIT OF 60/273,585 03/06/2001

\*\* FOREIGN APPLICATIONS VERIFIED: *W*

PG-PUB	DO NOT PUBLISH <input checked="" type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	AP01-005
Verified and Acknowledged Examiners's initials <i>W</i>		
TITLE : Bit-by-bit Vt-correction operation for nonvolatile semiconductor one-transistor cell, nor-type flash EEPROM		

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
<i>70/25/02</i> <i>10-23-02</i> Assistant Examiner		Total Claims <i>25</i>	Print Claim for O.G. <i>1</i>
ISSUE FEE		DRAWING	
Amount Due <i>640</i>	Date Paid <i>12-9-02</i>	Sheets Drwg. <i>15</i>	Figs. Drwg. <i>17</i>
PRIMARY EXAMINER <i>HUANHOANG</i> Primary Examiner		Print Fig. <i>7A</i>	
PREPARED FOR ISSUE		Application Examiner <i>Haves</i> <i>10/30/02</i>	
DISCLAIMER		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

Drawing

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